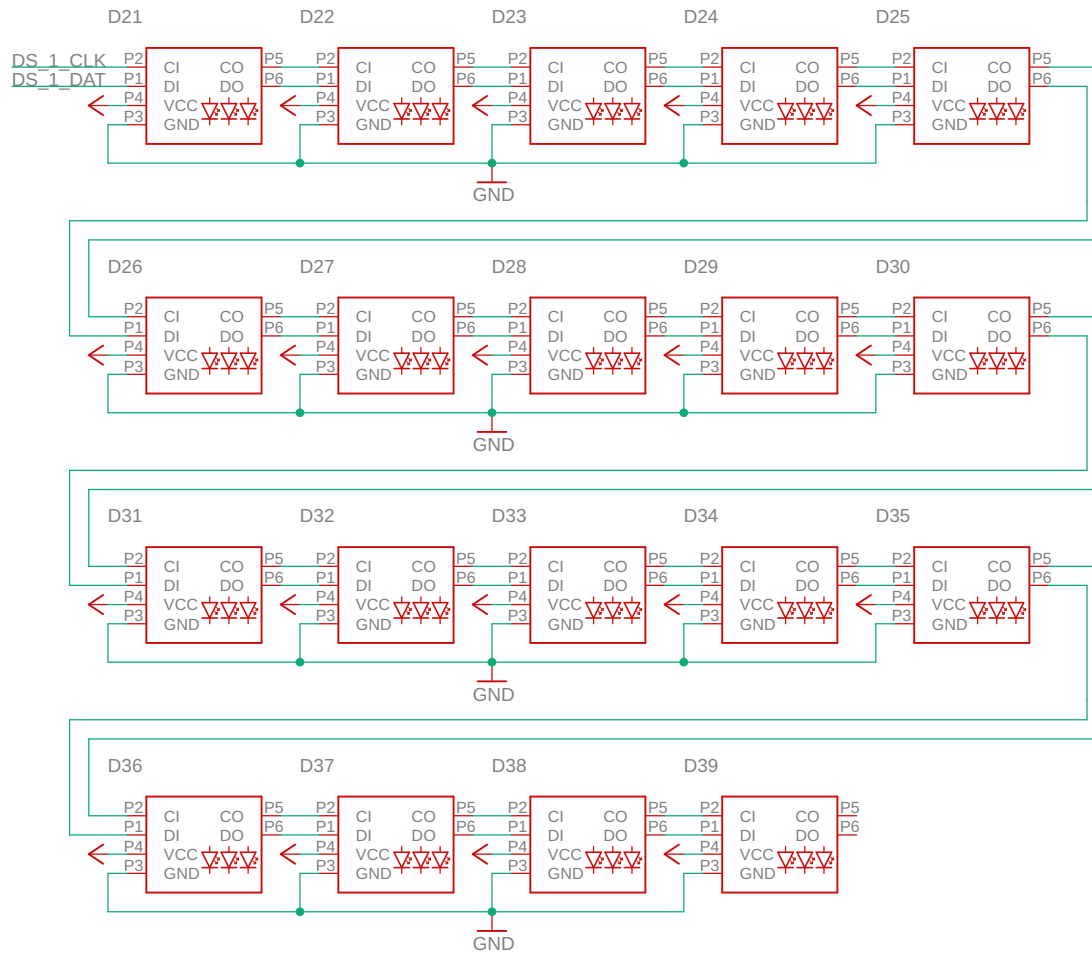


Test points on bottom of PCB allow initial Fuses and Bootloader programming via ICSP

Note: We chose to avoid outdated SPI-interface terminology in this design. If you have questions, please visit : <https://www.oshwa.org/a-resolution-to-redefine-spi-signal-names/>

Outer Ring LEDs



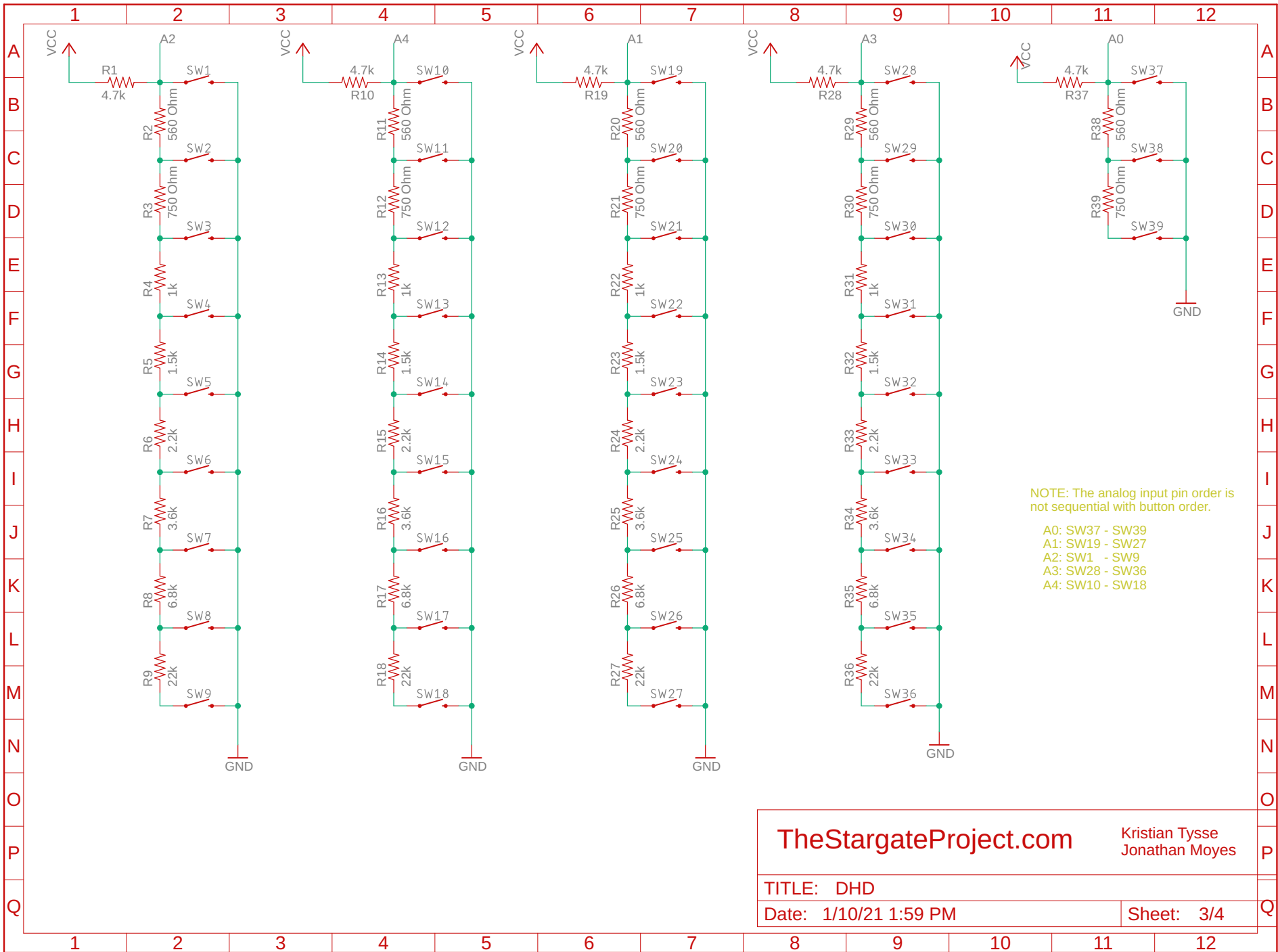
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